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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/611,314	06/30/2003	Joseph Edward Pekarek	AWR-P03008	3997
75	7590 05/26/2006		EXAMINER	
Robert Hart 10th Floor, Suite H528			PATEL, SHAMBHAVI K	
28 East Jackson Building			ART UNIT	PAPER NUMBER
Chicago, IL 60604		2128		
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Please find below and/or attached an Office communication concerning this application or proceeding.

	Paper No(s)/Mail Date _
ı	U.S. Patent and Trademark Office

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)

5) Notice of Informal Patent Application (PTO-152)

Interview Summary (PTO-413)
Paper No(s)/Mail Date.

6) Other:

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#### **DETAILED ACTION**

Claim 1 is pending.

### **Priority**

Acknowledgment is made of applicant's claim for priority to provisional application 60/392,647 filed on 06/29/2002.

# **Drawings**

1. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the final transfer function calculated in the last step of claim 1 must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be

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notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

2. The drawings are objected to because they do not contain reference numbers for the block.

Figures 31, 33, 37, and 39-44 do not have block descriptions.

# Double Patenting

A rejection based on double patenting of the "same invention" type finds its support in the language of 35 U.S.C. 101 which states that "whoever invents or discovers any new and useful process ... may obtain a patent therefor ..." (Emphasis added). Thus, the term "same invention," in this context, means an invention drawn to identical subject matter. See *Miller v. Eagle Mfg. Co.*, 151 U.S. 186 (1894); *In re Ockert*, 245 F.2d 467, 114 USPQ 330 (CCPA 1957); and *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970).

A statutory type (35 U.S.C. 101) double patenting rejection can be overcome by canceling or amending the conflicting claims so they are no longer coextensive in scope. The filing of a terminal disclaimer cannot overcome a double patenting rejection based upon 35 U.S.C. 101.

3. Claim 1 is provisionally rejected under 35 U.S.C. 101 as claiming the same invention as that of claim 1 of copending Application No. 10/420,955. This is a <u>provisional</u> double patenting rejection since the conflicting claims have not in fact been patented.

# Claim Rejections - 35 USC § 101

35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

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4. Claim 1 is rejected under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter. The Examiner asserts that the current state of the claim language is such that a reasonable interpretation of the claim would *not result in a tangible product*. The method claim calculates a transfer function for the first and second blocks, but this is not a tangible output.

# Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

5. Claim 1 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

The Examiner is unclear regarding how the first and second steps of the claim are different from one another. The first step is directed to obtaining the frequency domain transfer function, and the second step is directed to computing the frequency domain transfer function. The examiner interprets these two steps to be functionally equivalent.

Step 3 of the claim reads 'inputting frequency dependent impedance of the input to the second block and the output of the first block'. The Examiner asserts that the meaning of this step is vague because the meaning of the term 'input' is not clear.

The Examiner asserts that the meaning of step 5 is vague because claim utilizes variables B1(f), B2(f), and B3(f), but the variables are not defined in the claim.

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The claim as a whole is incomplete because calculating an overall transfer function for the first and seconds block does not complete the step of determining impedance mismatch (as stated in the preamble).

### Claim Interpretation

6. The subsequent prior art rejections are asserted in view of the following claim analysis.

Numerous 112 rejections have been applied against the claim. MPEP section 2143.03 addresses the issue of applying prior art against such claims:

A claim limitation which is considered indefinite cannot be disregarded. If a claim is subject to more than one interpretation, at least one of which would render the claim unpatentable over the prior art, the examiner should reject the claim as indefinite under 35 U.S.C. 112, second paragraph (see MPEP § 706.03(d)) and should reject the claim over the prior art based on the interpretation of the claim that renders the prior art applicable. Ex parte Ionescu, 222 USPQ 537 (Bd. Pat. App. & Inter. 1984) (Claims on appeal were rejected on indefiniteness grounds only; the rejection was reversed and the case remanded to the examiner for consideration of pertinent prior art.). Compare In re Wilson, 424 F.2d 1382, 165 USPQ 494 (CCPA 1970) (if no reasonably definite meaning can be ascribed to certain claim language, the claim is indefinite, not obvious) and In re Steele, 305 F.2d 859,134 USPQ 292 (CCPA 1962) (it is improper to rely on speculative assumptions regarding the meaning of a claim and then base a rejection under 35 U.S.C. 103 on these assumptions).

See also section 2173.06 (Prior Art Rejection of Claim Rejected as Indefinite):

All words in a claim must be considered in judging the patentability of a claim against the prior art. In re Wilson, 424 F.2d 1382, 165 USPQ 494 (CCPA 1970). The fact that terms may be indefinite does not make the claim obvious over the prior art. When the terms of a claim are considered to be indefinite, at least two approaches to the examination of an indefinite claim relative to the prior art are possible. First, where the degree of uncertainty is not great, and where the claim is subject to more than one interpretation and at least one interpretation would render the claim unpatentable over the prior art, an appropriate course of action would be for the examiner to enter two rejections: (A) a rejection based on indefiniteness under 35 U.S.C. 112, second paragraph; and (B) a rejection over the prior art based on the interpretation of the claims which renders the prior art applicable. See, e.g., Ex parte Ionescu, 222 USPQ 537 (Bd. App. 1984). When making a rejection over prior art in these circumstances, it is important for the examiner to point out how the claim is being interpreted. Second, where there is a great deal of confusion and uncertainty as to the proper interpretation of the limitations of a claim, it would not be proper to reject such a claim on the basis of prior art. As stated in In re Steele, 305 F.2d 859, 134 USPQ 292 (CCPA 1962), a rejection under 35 U.S.C. 103 should not be based on considerable speculation about the meaning of terms employed in a claim or assumptions that must be made as to the scope of the claims. The first approach is recommended from an examination standpoint because it avoids piecemeal examination in the event that the examiner's 35 U.S.C. 112, second paragraph

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rejection is not affirmed, and may give applicant a better appreciation for relevant prior art if the claims are redrafted to avoid the 35 U.S.C. 112, second paragraph rejection.

There is a great deal of confusion and uncertainty as to the proper interpretation of the limitations of claim 1, and thus it would not be proper to reject such a claim on the basis of prior art. However, in the interests of compact prosecution, such an interpretation will be nonetheless provided. The meaning of the limitations of steps 3 and 5 of claim 1 is unknown (i.e. the terms 'input', B1(f), and B2(f) are vague and indefinite). The Examiner is unable to differentiate between steps 1 and 2 of the claims. However, a careful study of the specification and claims indicates the output impedance of the first block and the input impedance of the second block are used to calculate the transfer function of the mismatch block, and this transfer function is then used to calculate the transfer function of the overall system.

# Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

- 1. Determining the scope and contents of the prior art.
- 2. Ascertaining the differences between the prior art and the claims at issue.
- 3. Resolving the level of ordinary skill in the pertinent art.

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4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

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This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

7. Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kim et al. ('Full Software Analysis and Impedance Matching of Radio Frequency CMOS Integrated Circuits') herein referred to as 'Kim', in view of Lathi ('Linear Systems and Signals).

Kim discloses performing simulated *impedance matching* within an itegrated circuit (*Kim: abstract*). The prior art discloses breaking the circuit into blocks and calculating the input impedance of the chip (*Kim:* section III 'Analysis of Z(in) and Design of the Impedance Matching Block' paragraphs 1-3). Using this, the off-chip input impedance matching conditions are derived (*Kim:* 'Introduction' paragraph 4). Kim does not disclose performing impedance matching using transfer functions.

Lathi teaches calculating the transfer function of a block using equations 4.40 and 4.41 on page 279. The equation relates the output of the block to the input of the block. Step 4 of the claim derives the transfer function is a relationship between the input and output impedances of the block. Thus, equation 4.41 of the prior art is analogous to the equation derived in step 4 of the claim.

Lathi discloses calculating the transfer function of a system by relating the input coming into the system with the output coming into the system (Lathi: page 280). The equation shown in figure 4.9 is analogous to the equation in step 5 of the claim. To determine the transfer function of the overall system, the Applicant discloses multiplying the input of the final block (B1(f) \* G(Zout1(f), Zin2(f))) with the transfer function of the final block (B2(f) \* G(Zout2(f), Zin3(f)) \* B3(f)). Similarly, Lathi discloses that the output of the system is a product of the input (F(s)) and the transfer function of the final block (H(s)).

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It would have been obvious to one of ordinary skill in the art to calculate the behavior of the system using a transfer function because transfer functions are well-known in the art. It is convenient to define a system using the transfer function (*Lathi*: page 279 paragraph 1) and doing so in the frequency domain enables the determining of the system response to an arbitrary input (*Lathi*: page 283 paragraph 3).

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should

be directed to Shambhavi Patel whose telephone number is (571) 272-5877. The examiner can normally

be reached on Monday-Friday, 8:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor,

Kamini Shah can be reached on (571)272-22792279. The fax phone number for the organization where

this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application

Information Retrieval (PAIR) system. Status information for published applications may be obtained

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Business Center (EBC) at 866-217-9197 (toll-free).

SKP

Shambhavi Patel Examiner

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SUPERVISORY PATENT EXAMINE